

**METHOD FOR EFFECTIVE BINARY TRANSLATION BETWEEN  
DIFFERENT INSTRUCTION SETS USING EMULATED SUPERVISOR  
FLAG AND MULTIPLE PAGE TABLES**

**ABSTRACT OF THE DISCLOSURE**

The present invention relates to a microprocessor based computer system having binary translation support to achieve high performance without compatibility problems. The computer system includes a support software layer that enables the execution of foreign application and operating system software without degradation during execution. The support software layer includes an emulated foreign supervisor flag and means for porting foreign software including the operating system to the host platform. The host platform includes host and foreign virtual space spaces and two page tables supported in hardware to minimize the time required for translation between virtual to physical addresses. A flag in a page table entry marks pages in the virtual memories having data that is accessible only to the host platform's supervisor. When the computer system processor is not operating in a foreign supervisor mode, an attempt to access the virtual space generates an exception trap.

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